REMARKS

Allowable subject matter

The Examiner objects to claims 3, 9-10, 15, and 21-22 as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claim.

The applicant rewrites rejected independent claims 1 and 13 to include limitations from objected claims 3 and 15, respectively. Claims 1 and 13 are now allowable, along with associated dependent claims.

The applications believe, in view of the arguments presented below, that the independent claims associated with objected claims 9-10 and 21-22 are allowable, and hence, the applicant does not rewrite these claims in independent form.

Claim Rejections Under 35 USC §102

Claims 1-2, 4-8, 11-14, 16-20 and 23-34 are rejected under 35 USC §102(e) as being anticipated by Litt, U.S. Patent No. 7,051,239.

As stated above, the applicants amend independent claims 1 and 13 to include limitations from objected claims 3 and 15, respectively. Claims 1 and 13 are now allowable, along with associated dependent claims.

Independent claim 7 recites *identifying data packets; ... modifying the data packets responsive to the identifying*. Claim 19 recites similar limitations. In office actions dated 8/14/2007 and 4/9/2007, the Examiner fails to specifically discuss these limitations. Instead, while rejecting claims 7 and 19, the Examiner discusses limitations from claim 1, which are different from the limitations of claims 7 and 19 recited here.

While rejecting claim 7, the Examiner alleges that "Litt discloses a system that can test a process over bus lines with an OCLA used to test data on a cache over a bus."[1] Litt's OCLA is "capable of reading the data from the cache memory to a register that can be addressed by the processor,"[2] thereby "providing a robust history log for debugging and performance testing of the integrated circuit."[3] Litt further discloses the following about the operation of the OCLA:

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[1] Office action dated 8/14/2007, page 3, paragraph 8.
[2] Litt, column 7, lines 53-57.
[3] Litt, column 4, lines 54-59.

"the OCLA 125 has three major modes: (1) disabled; (2) acquisition; and (3) idle. When disabled, the OCLA cannot acquire data, and most of its resources may be used for other purposes. As noted above, when the OCLA 125 is disabled, all of the operable cache sets may be used for normal memory operations. In acquisition mode, the OCLA 125 acquires internal processor state as data, and stores selected state data in the pre-selected cache memory set(s). In the idle mode, the OCLA cannot acquire data, but data stored in the cache set(s) may be read."[4]

That is, the OCLA acquires internal processor state as data, and stores selected state data in the pre-selected cache memory set(s), or reads stored data. Although Litt discloses acquiring, storing and reading data, Litt, however, does not disclose that the OCLA modifies data packets, as would be required by the recited limitation. Acquiring, storing or reading data packets, which Litt performs, cannot disclose the recited modifying (i.e. changing) data packets. For at least this reason, claims 7 and 19 are in condition for allowance, along with associated dependent claims.
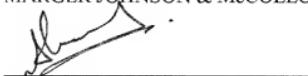
CONCLUSION

Applicant requests reconsideration in view of the foregoing remarks. The Examiner is encouraged to telephone the undersigned at (503) 222-3613 if it appears that an interview would be helpful in advancing the case.

Customer No. 32331   Respectfully submitted,

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[4] Litt, column 8, lines 28-38, underline added.